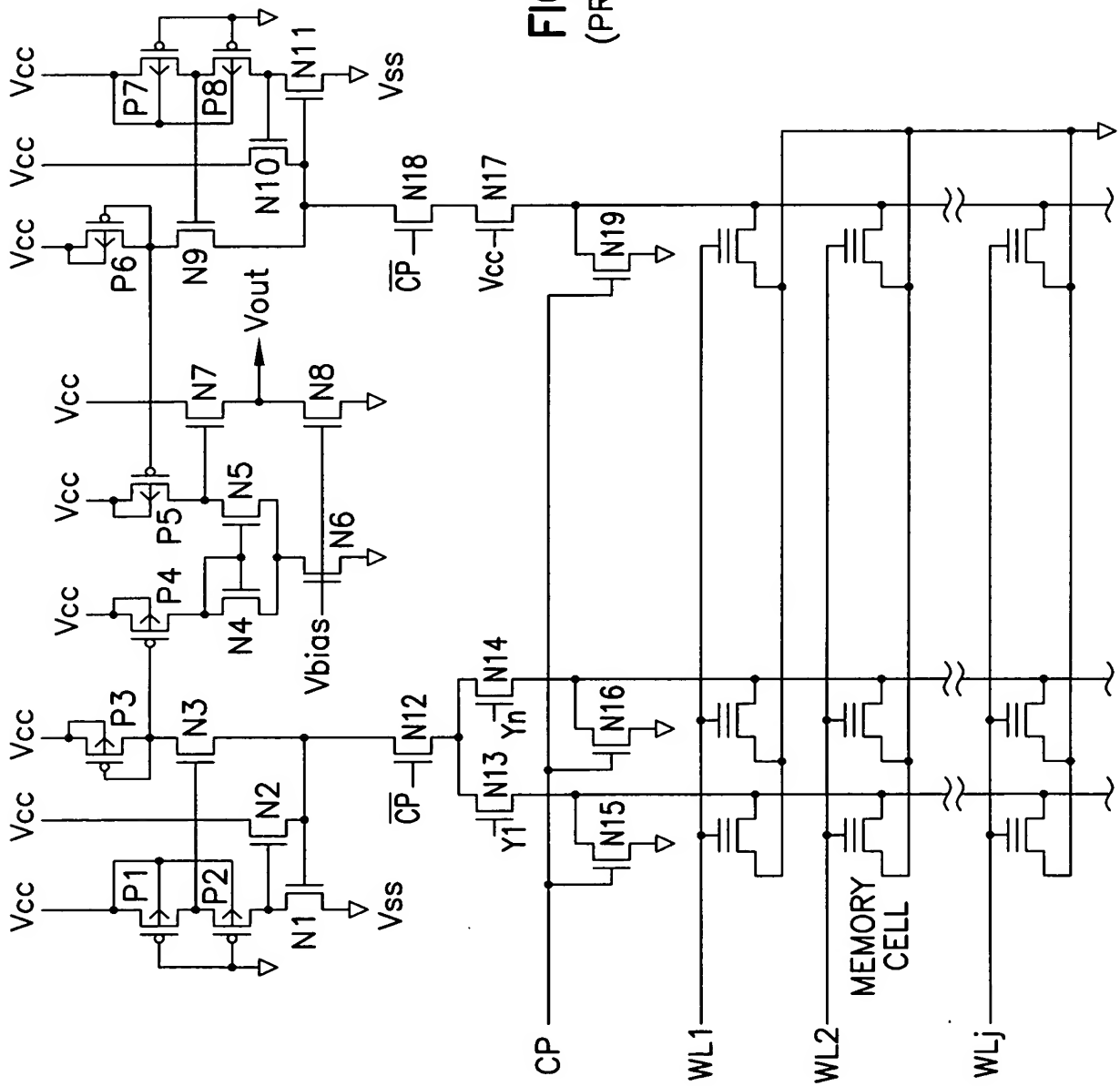


FIG. 1A (PRIOR ART)

FIG. 1B
(PRIOR ART)



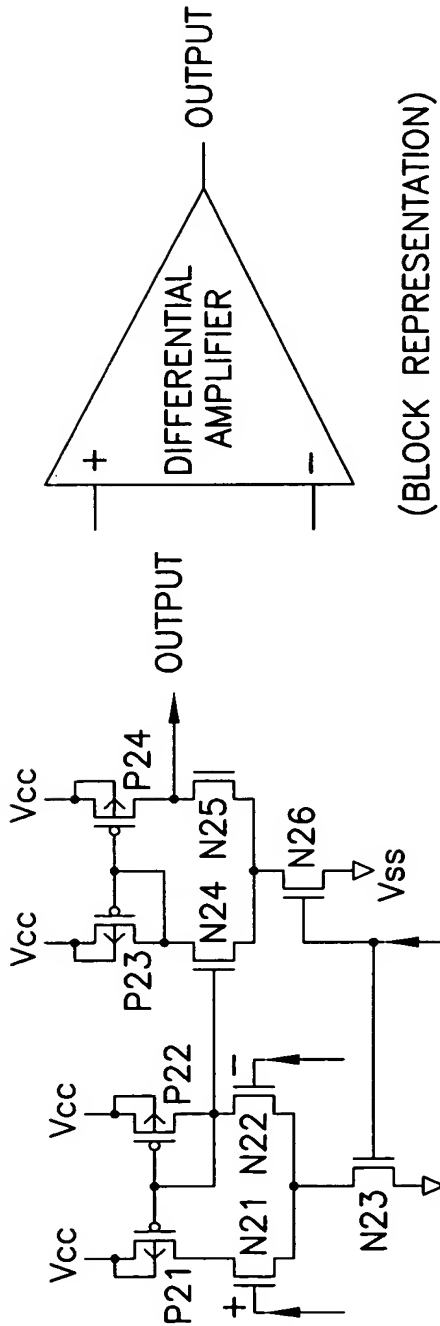
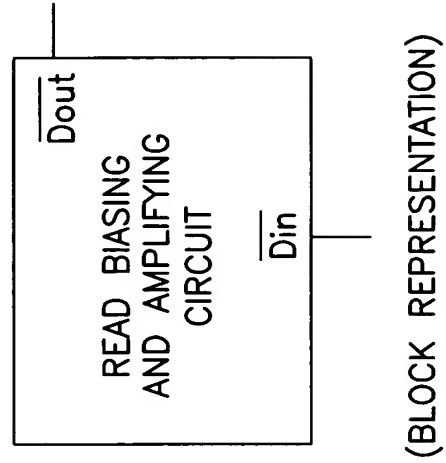


FIG. 2

(CIRCUIT SCHEMATIC)



(BLOCK REPRESENTATION)

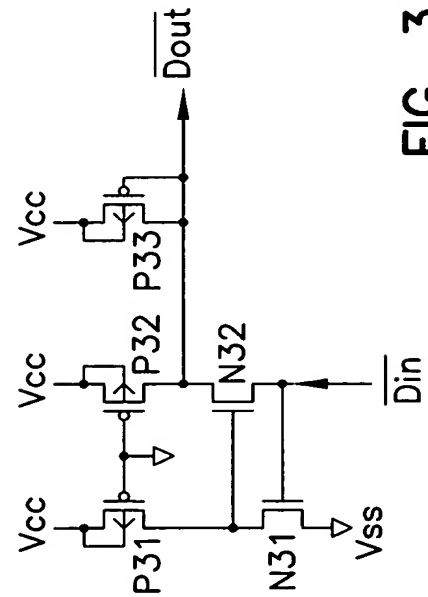


FIG. 3

(CIRCUIT SCHEMATIC)

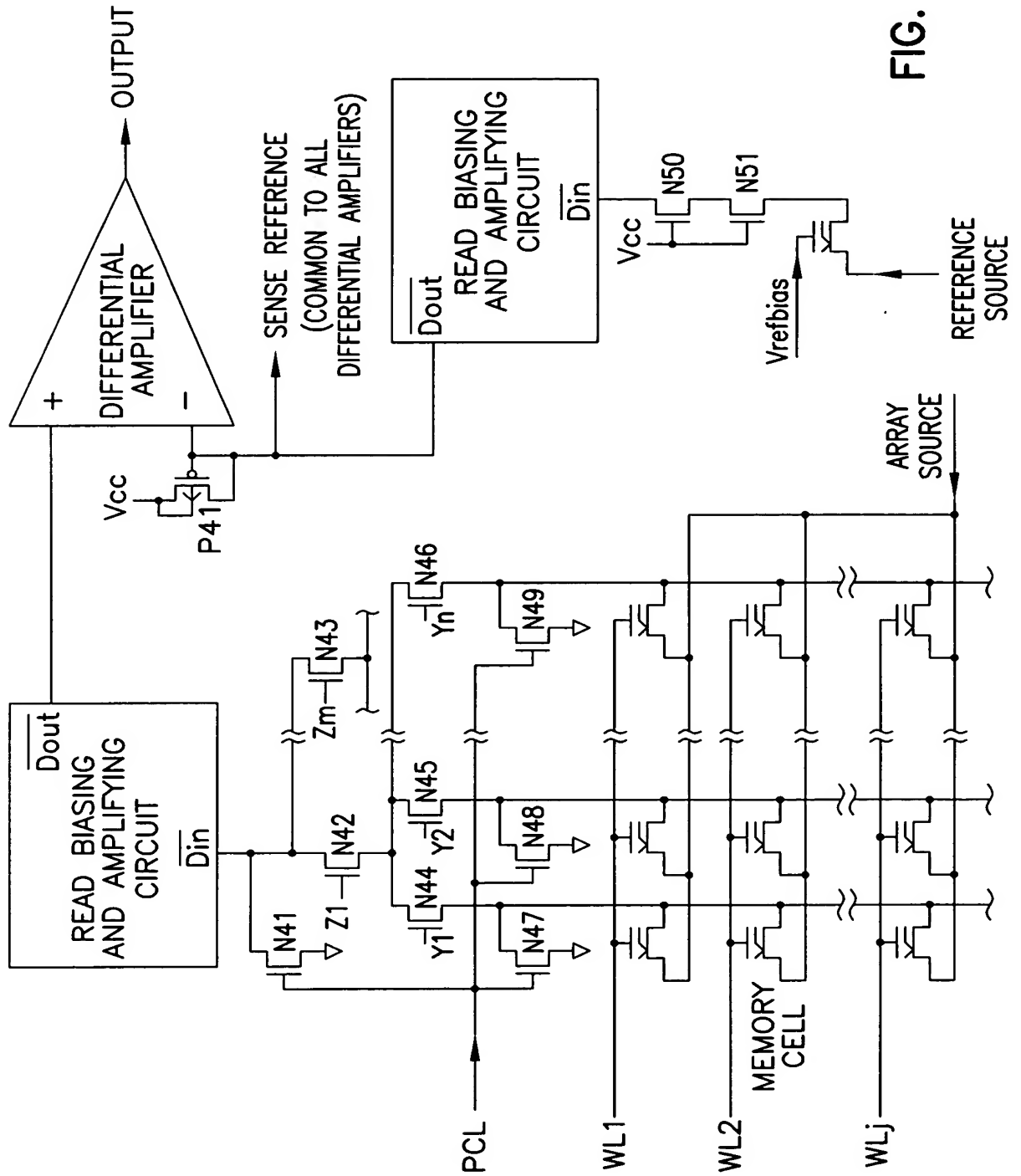


FIG. 4

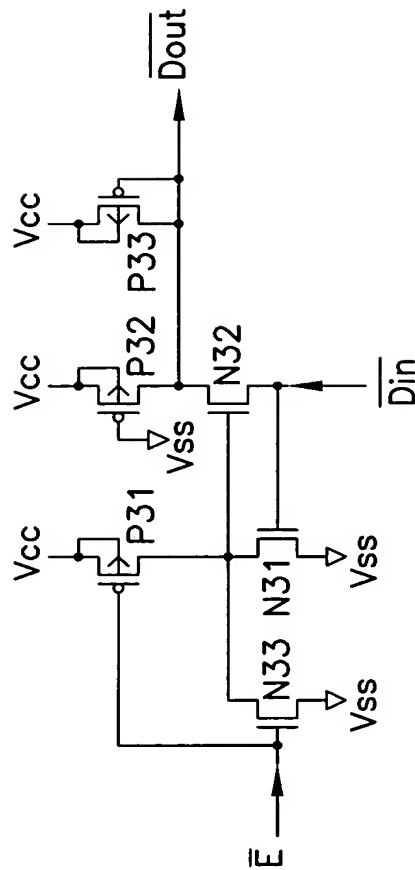


FIG. 5

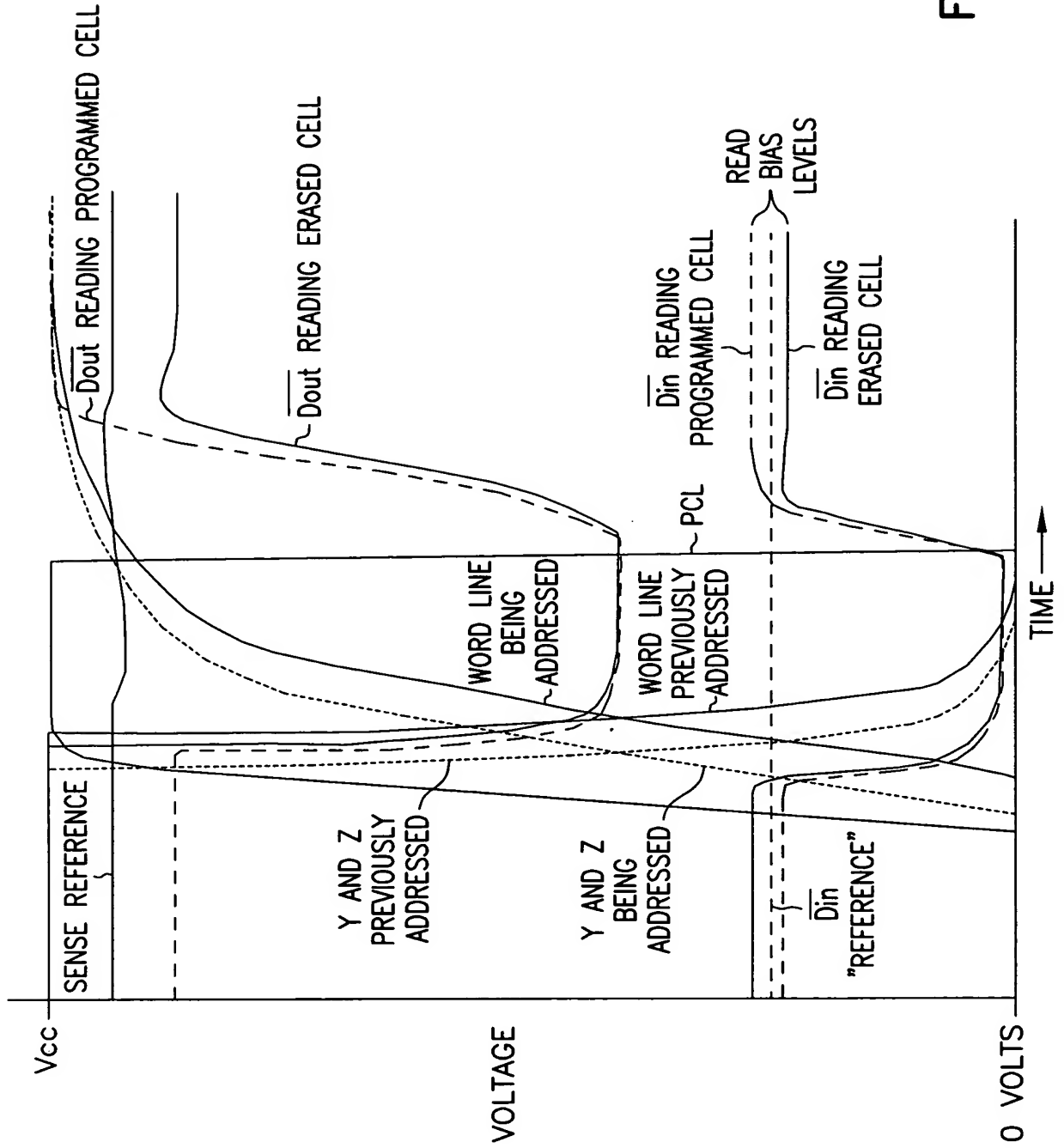


FIG. 6

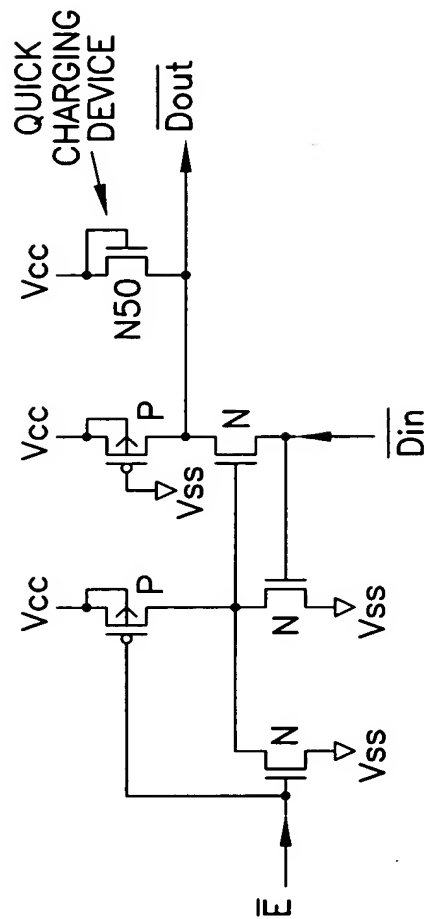


FIG. 7

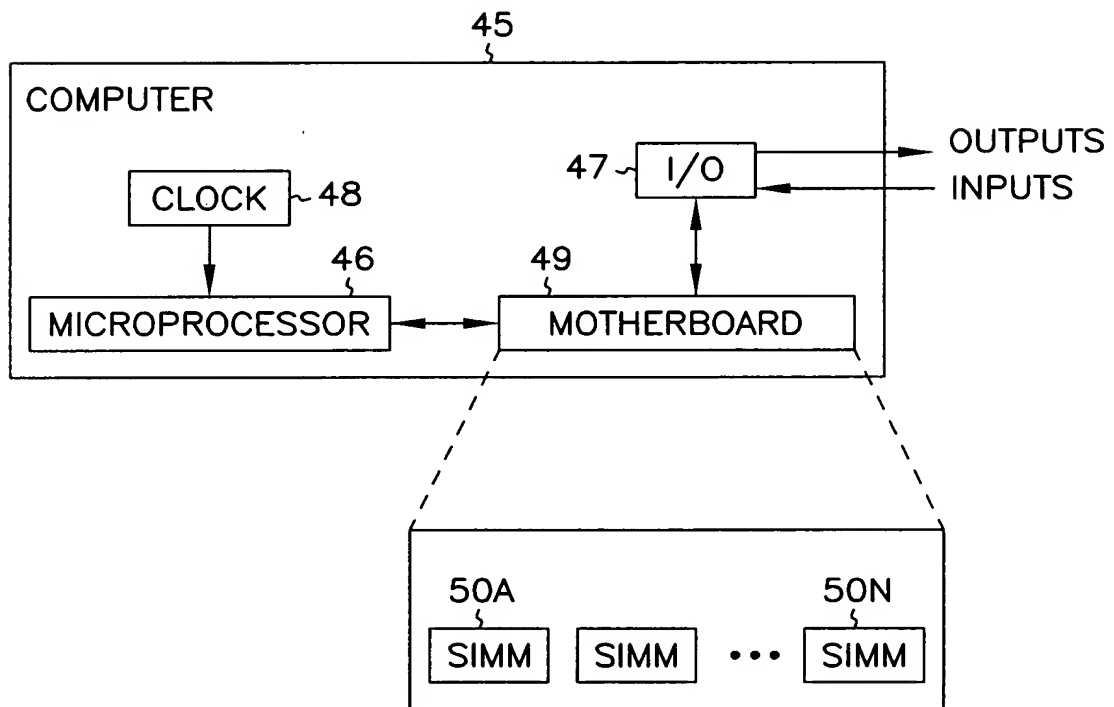


FIG. 8